

Jul-21-02 09:27pm From-SKJERVEN MORRILL LLP

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From:

Michael Cleveland

Subject:

09/351,544

Client/File:

M-10889

Date Sent:

Time Sent:

Fax operator:

This transmittal consists of 19 page(s), including this cover sheet. Return to: M. Cleveland.

Message:

Examiner Brock,

Here is a copy of the proposed Amendment for
discussion on Tuesday morning,

Respectfully Submitted,

Michael Cleveland

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Timothy K. Carns, Lee J. DeBruler, John L. Horvath, Michael J. Westphal
Assignee: Zilog, Inc.
Title: PROCESS TO IMPROVE HIGH PERFORMANCE CAPACITOR PROPERTIES IN INTEGRATED MOS TECHNOLOGIES
Serial No.: 09/351,544 Filing Date: July 12, 1999
Examiner: Brock II, P. Group Art Unit: 2815
Docket No.: M-10889 US (Formerly ZILG.204US0)

San Francisco, California
July 21, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

AMENDMENT AFTER FINAL

Dear Sir:

Responsive to the Official Action mailed on May 3, 2002, applicant responds to the Official Action as follows:

In the Claims:

Please replace claim 3 with the following (a marked up version is in the Appendix):

--3.(Twice Amended) A method of forming a capacitor in an integrated circuit comprising:

- forming a bottom electrode layer on a semiconductor body;
- forming a dielectric layer over at least a portion said bottom electrode;
- forming a top electrode layer over at least a portion of said dielectric layer;
- removing a portion of said top electrode layer to expose a portion of the dielectric layer;
- subsequently removing at least a portion of said exposed portion of the dielectric layer to expose a portion of said lower electrode layer;
- subsequently forming a conformal insulating layer over at least a portion of said exposed portion of the bottom electrode layer proximate to said exposed dielectric layer,

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